

Confirmation no. 8030

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| Applicant: | VAN SCHAIJK, <i>et al.</i> | Examiner: | Quinto, K. |
| Serial No.: | 10/574,030 | Group Art Unit: | 2811 |
| Filed: | March 27, 2006 | Docket No.: | NL031167US1 |
| Title: | TWO-TRANSISTOR MEMORY CELL AND METHOD FOR MANUFACTURING | | |

AMENDMENT AND RESPONSE

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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| Customer No. 65913 |
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Dear Sir:

In response to the non-final Office Action dated December 17, 2007, please consider the following amendments and remarks.

Amendments to the drawings are described at page 2 of this paper.

A complete listing of the Claims, to include any amendments thereto, begins on page 3 of this paper.

Remarks/Arguments follow on page 5.

Authorization is given to charge/credit Deposit Account 50-0996 (NXPS.442PA) all required fees/overages to enter this paper.